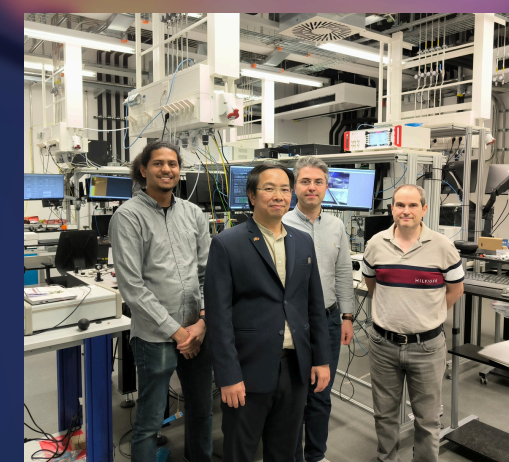
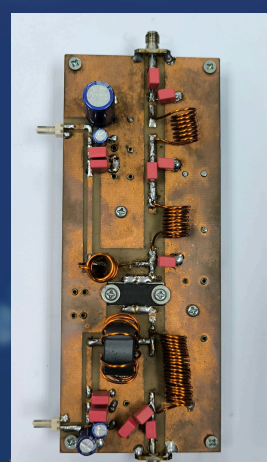
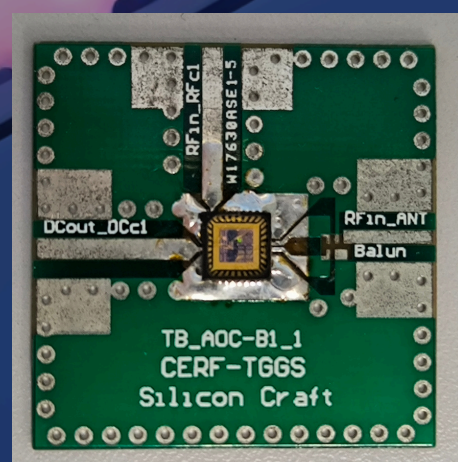


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4 + 1 Years Bachelor + Master

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CLEANROOM FACILITY WITH RF MEASUREMENT AND ON-WAFER PROBE STATION. ADVANCED MEASUREMENT TOOLS FOR HIGH-FREQUENCY CIRCUIT TESTING.

RFIC DESIGNED BY A FORMER PHD STUDENT IN COLLABORATION WITH SILICON CRAFT TECHNOLOGY PLC

POWER AMPLIFIER BY TGS STUDENT WITH WORLD RECORD IN POWER EFFICIENCY

MAIN BUILDING, RWTH AACHEN UNIVERSITY, GERMANY

TGS DEAN AND MEDE CHAIRMAN DURING THE FACULTY MEETING, RWTH AACHEN UNIVERSITY

FORMER TGS MASTER'S STUDENT AS A PHD CANDIDATE AT THE PHOTONIC IC LAB RWTH AACHEN UNIVERSITY, GERMANY

PROGRAM HIGHLIGHTS

VLSI ARCHITECTURE

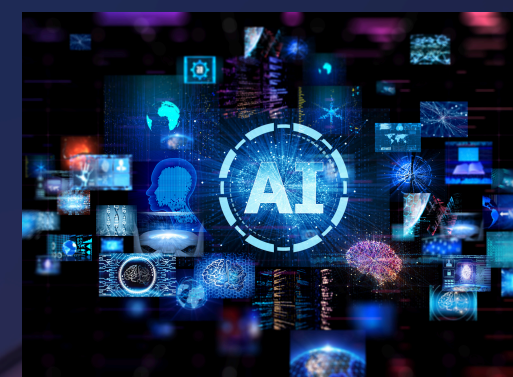
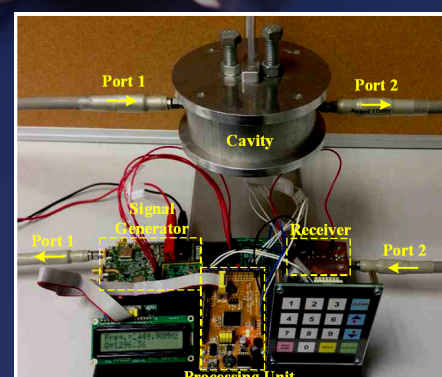
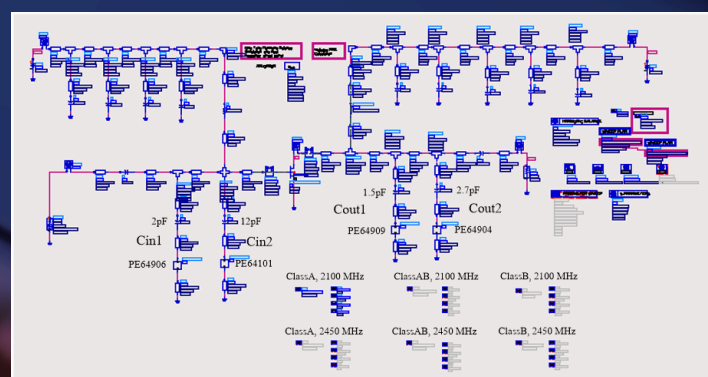
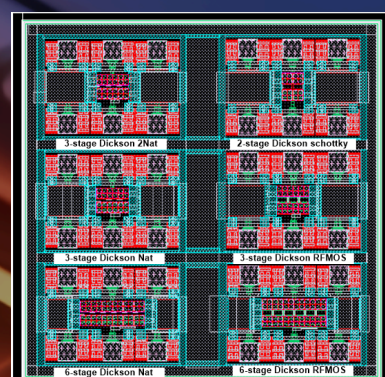
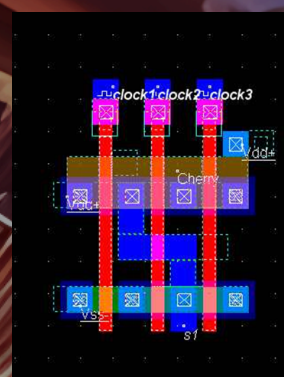
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RFIC DESIGN

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